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09/779123  
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PM98019C

Anantha R. Sethuraman

Christopher A. Seams

**Title: PLANARIZED SEMICONDUCTOR  
INTERCONNECT TOPOGRAPHY  
AND METHOD FOR POLISHING  
A METAL LAYER TO FORM  
INTERCONNECT**

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above and is addressed to: Assistant, Commissioner for Patents, Box Patent Application, Washington, DC 20231.

Derrick Brown

IDS  
RABELO  
8/24/01

Sir:

Applicant requests consideration of the references listed on the attached Form PTO-1449. The references listed on the Form PTO-1449 were previously cited in application serial no. 09/143,723 to which the captioned application is seeking priority under 35 U.S.C. § 120; therefore, a copy of each reference is not enclosed.

No fee is believed to be required. If a fee is required, the Commissioner is authorized to charge Conley, Rose & Tayon, P.C. Deposit Account No. 50-1505/5298-02501.

Respectfully submitted,

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Date: February 7, 2001